Reg. No:		
SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY: PUTTUR (AUTONOMOUS)		
B.Tech II Year II Semester Supplementary Examinations July-2021		
DIGITAL ELECTRONICS		
	(Electrical and Electronics Engineering)	
Time: 3 hours Max. Marks: 60		
$\frac{PART-A}{7}$		
1	(Answer all the Questions $5 \ge 2 = 10$ Marks) a Convert the following to gray code. $(10110011)_2$	2M
7	<ul><li>b Define Multiplexer.</li></ul>	2M
	c What is the difference between Latch and Flip – flop?	2M
	d Define FAN IN and FAN OUT.	<b>2M</b>
	e What are the different types of semiconductor memories?	<b>2M</b>
	$\frac{\mathbf{PART} \cdot \mathbf{B}}{\mathbf{H} \mathbf{E} \cdot \mathbf{E}} = 10 - 50 \mathbf{N} \mathbf{E} + \mathbf{E}$	
	(Answer all Five Units 5 x $10 = 50$ Marks)	
2	UNIT-I Connect the following to Decimal on Island to Octob	<b>2</b> 3.4
2	<ul> <li>a Convert the following to Decimal and then to Octal</li> <li>i) (10110011)<sub>2</sub></li> <li>ii) (1234)<sub>16</sub></li> </ul>	5M
	<b>b</b> Perform the subtraction by using 1's complement for the given $(10101 - 11011)$ .	5M
	OR	
3	<b>a</b> Explain Different Types of binary codes and give the examples.	5M
	<b>b</b> Simplify the following Boolean expressions:	5M
	i) $F = (A+B)(A'+C)(B+C)$ . ii) $F = XY+XYZ+XYZ'+X'YZ$	
	UNIT-II	
4	a Minimize the following Boolean function using K-Map	5M
	$F(A, B, C, D) = \Sigma m(0, 2, 4, 6, 8, 10, 12, 14).$	5M
	<b>b</b> What is Decoder? design3:8 decoder. <b>OR</b>	5111
5	<b>a</b> Minimize the given Boolean function $F(A,B,C,D) = \Sigma m(0,1,2,3,6,7,13,15)$ using	5M
	tabulation method.	
	<b>b</b> Design 2-bit comparator with Logic diagram.	<b>5M</b>
	UNIT-III	
6	<b>a</b> Design D Flip Flop by using SR Flip Flop Explain the operation with truth table.	6M
	<b>b</b> Write the differences between combinational and sequential circuits.	<b>4M</b>
-		<b>~N4</b>
7	<ul><li>a Explain working of Master Slave Flip flop with neat diagram.</li><li>b Design T Flip Flop by using JK Flip Flop and draw the timing diagram.</li></ul>	5M 5M
	<b>b</b> Design T Flip Flop by using JK Flip Flop and draw the timing diagram.	3111
8		10M
0	OR	10101
9		10M
	UNIT-V	
10	Implement the following Boolean function using PLA	
	i) $F(w,x,y,z) = \Sigma m(0,1,3,5,9,13)$ ii) $F(w,x,y,z) = \Sigma m(0,2,4,5,7,9,11,15)$	<b>10M</b>
	OR CROM	1075
11	What is ROM organization? Explain about Different types of ROM.	10M

**R18** 

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